

Fig. 1

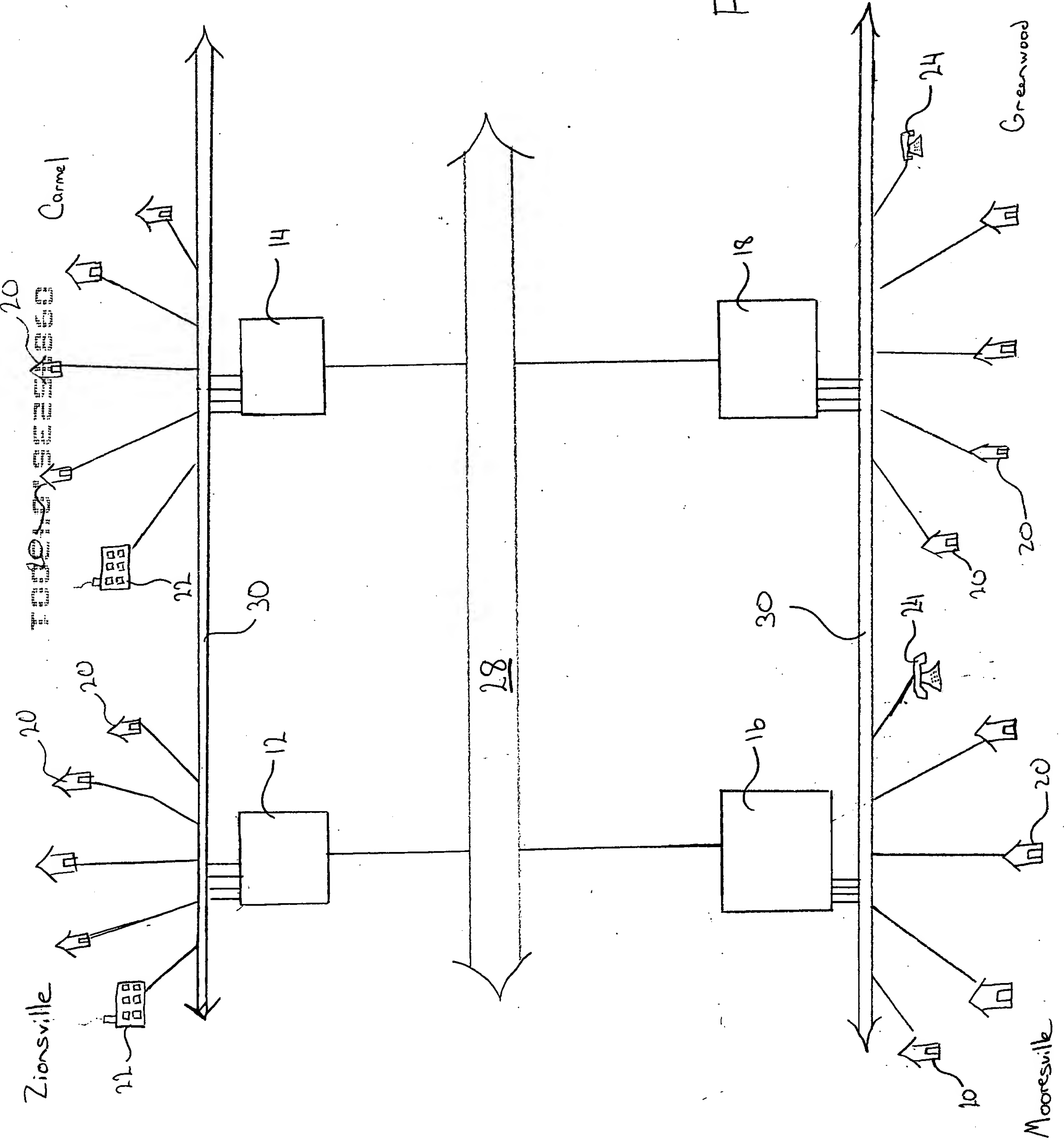


FIG. 2 is a block diagram of a system for processing data. The system includes a user input and display (51) connected to a hardware/software interface (50). The interface (50) is connected to a network (28) and a telecommunications system (30). The interface (50) also includes a buffer (53) and a series of registers (54, 55, 56). The interface (50) is connected to a data base (57), a temporary file (58), and a mapping device (59).

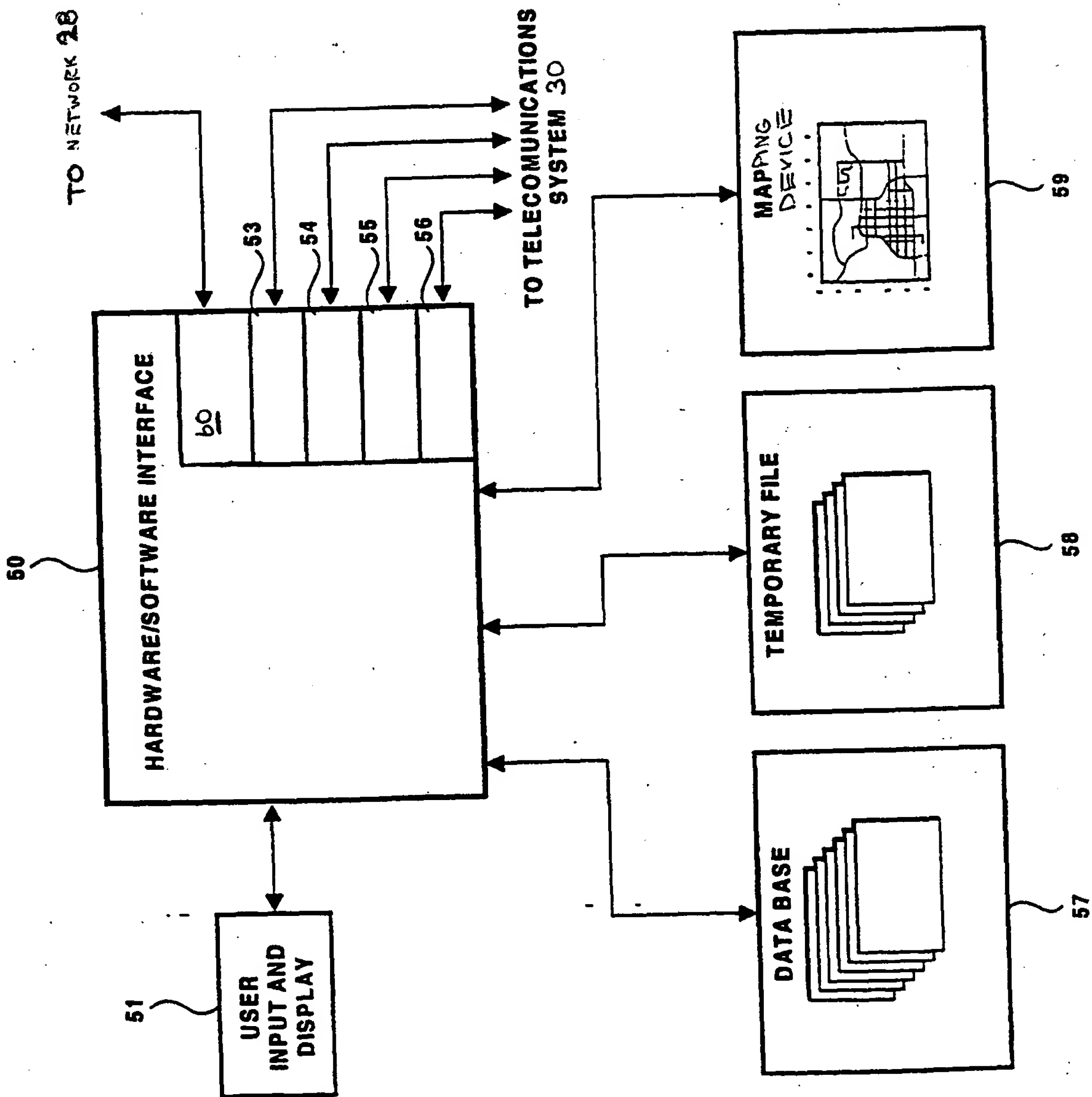


Fig. 2

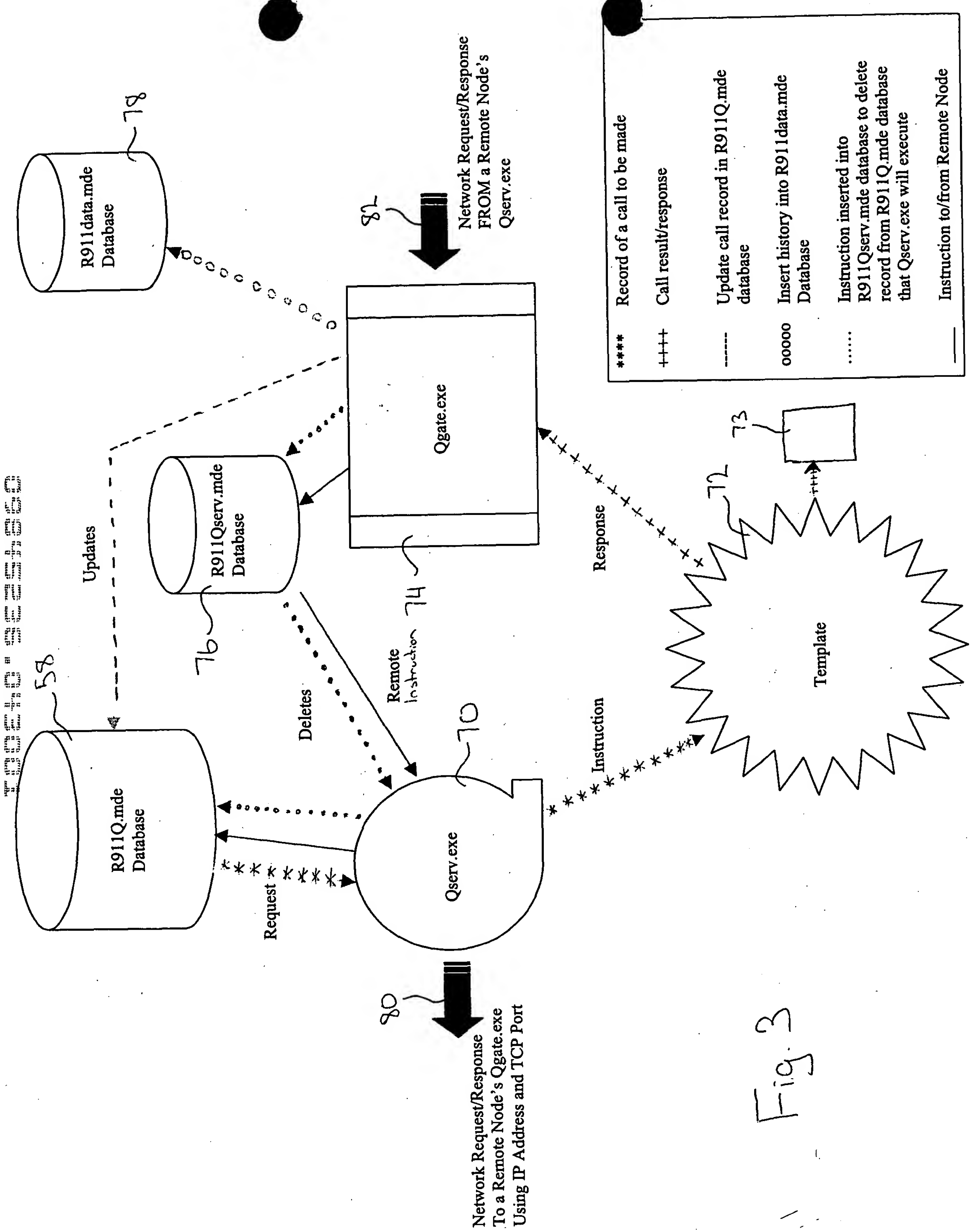


Fig. 3

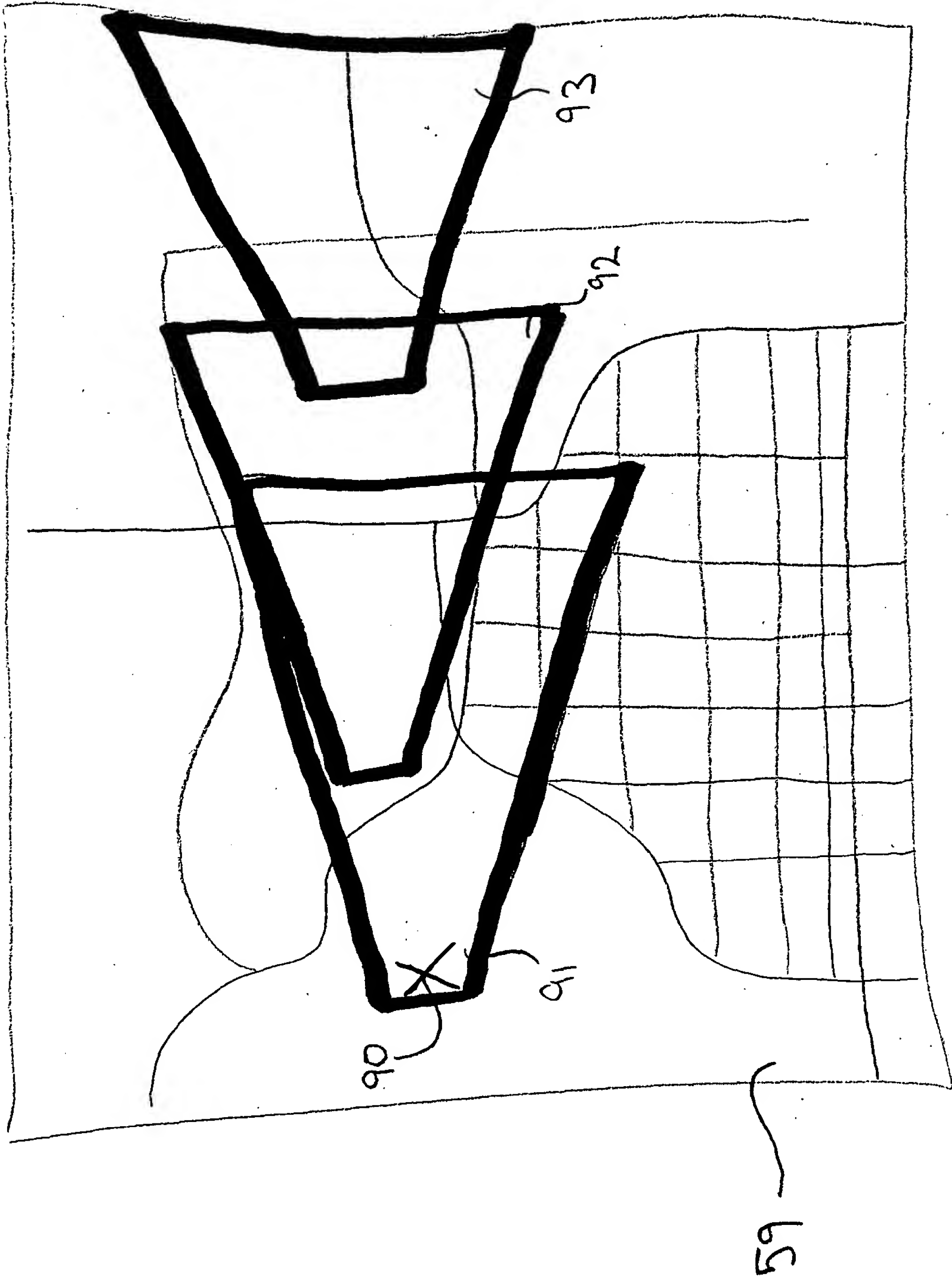


Fig. 4